

FIG. 1

401 INPUT PORT	402 INPUT LABEL	403 FEC	404 OUTPUT PORT	405 OUTPUT LABEL	406 DC FLAG
1	2	W.X.Y.Z	1	—	1
3	1	a.b.c.d	1	—	0
1	2	W.X.Y.Z	2	—	0

FIG. 2

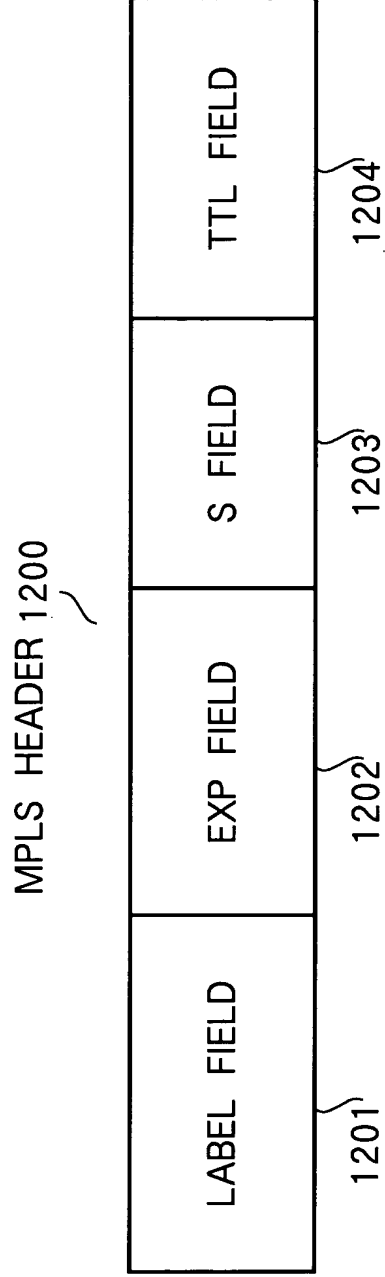


FIG. 3

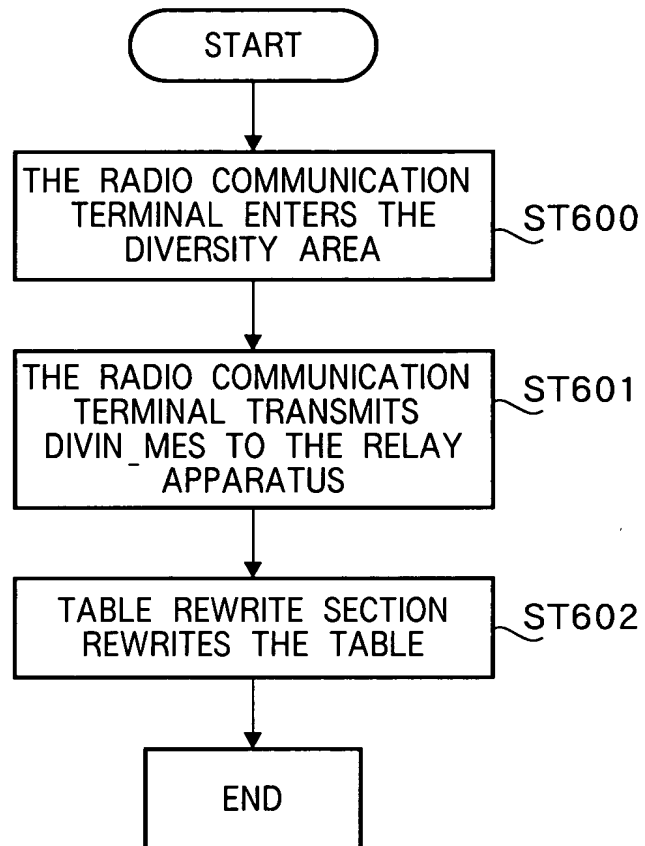


FIG. 4

DIVIN_MES1300

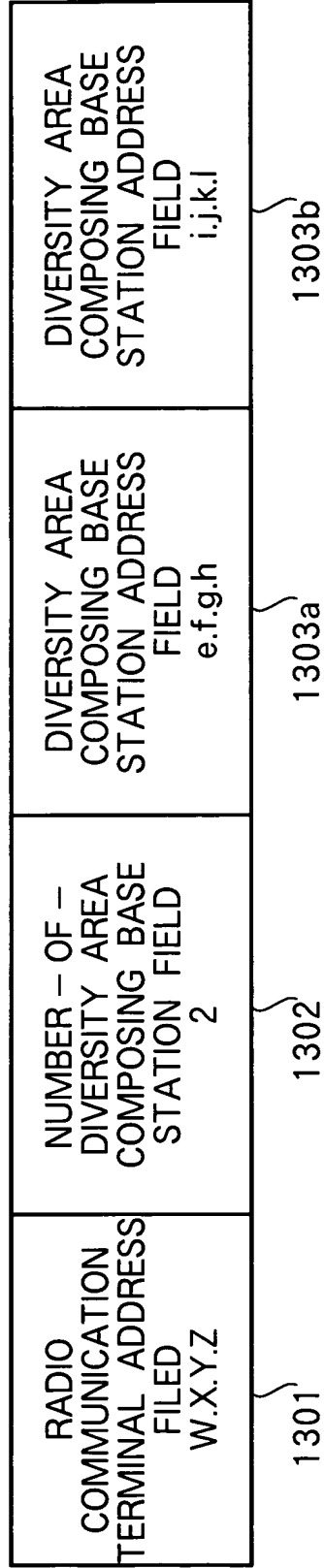


FIG. 5

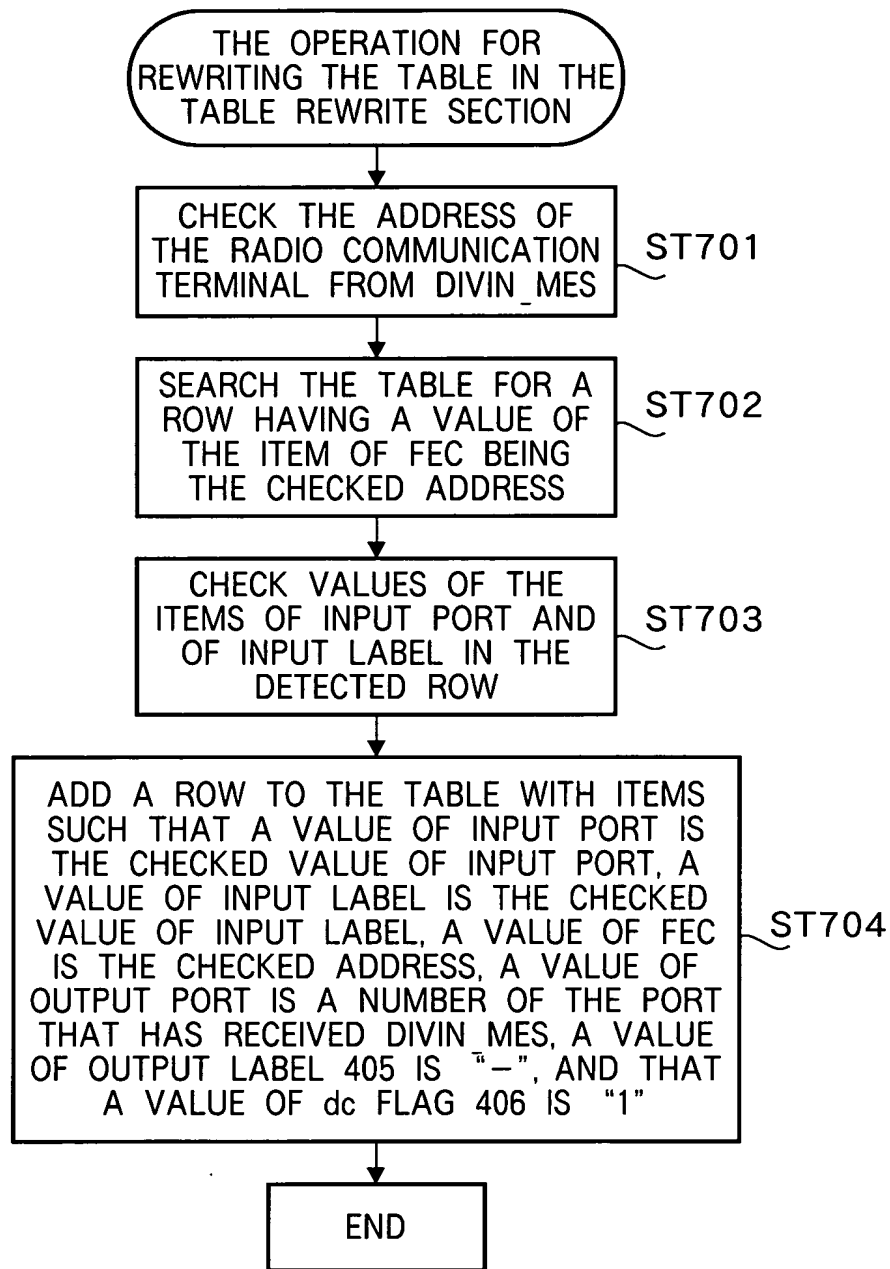


FIG. 6

401 INPUT PORT	402 INPUT LABEL	403 FEC	404 OUTPUT PORT	405 OUTPUT LABEL	406 DC FLAG
3	1	a.b.c.d	1	-	0
1	2	W.X.Y.Z	2	-	0

FIG. 7

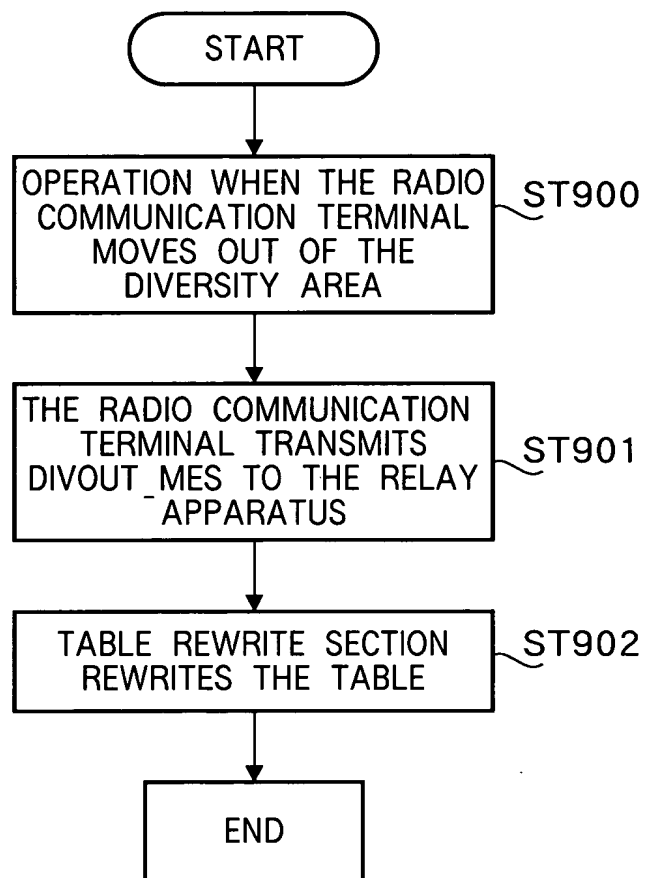


FIG. 8

DIVOUT_MES 1400

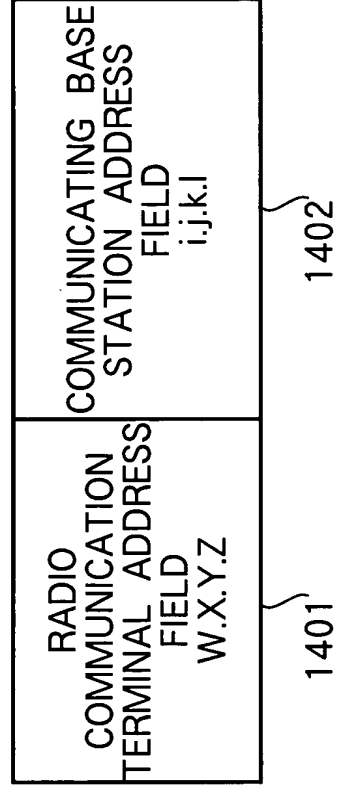


FIG. 9

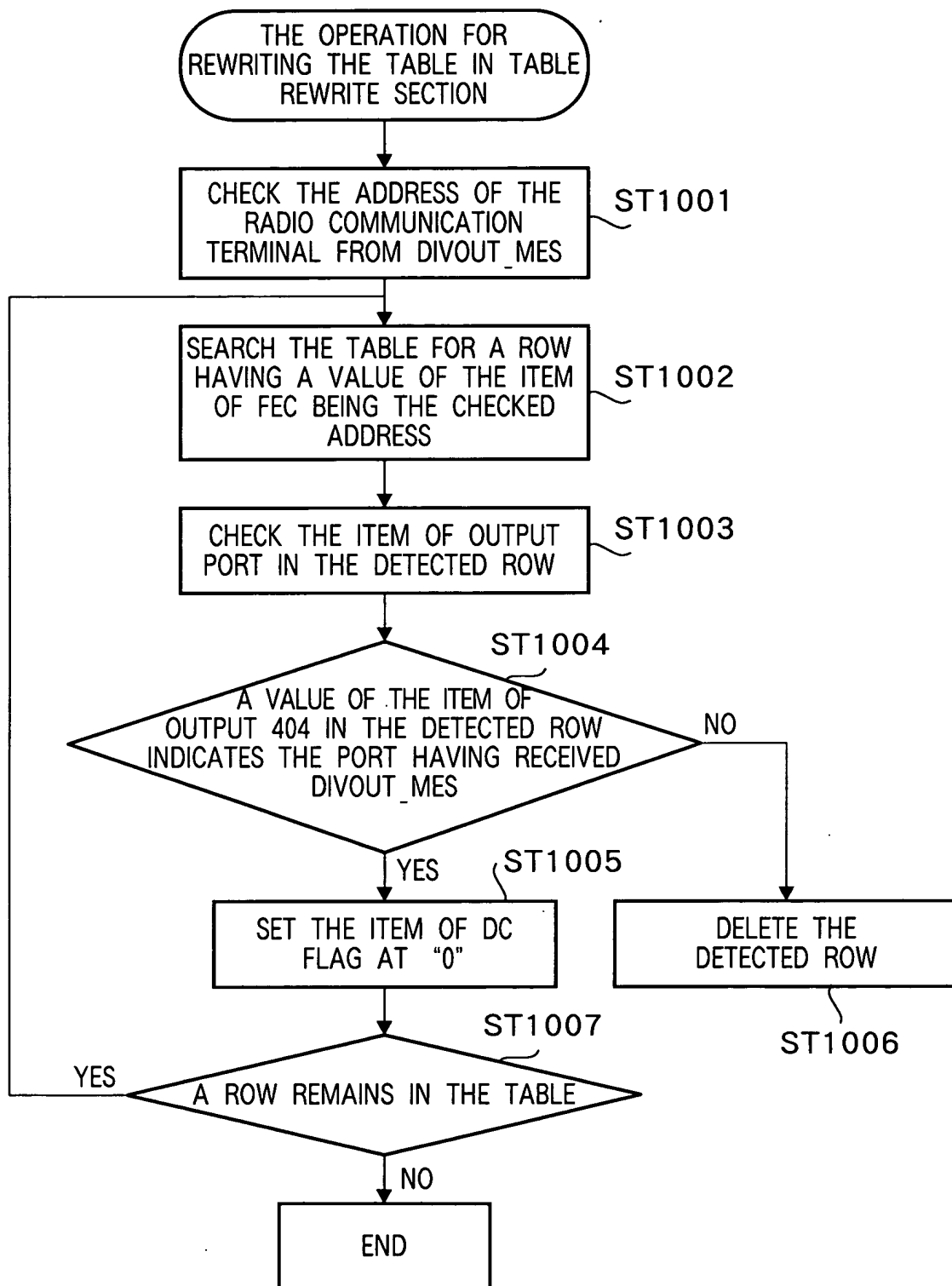


FIG.10

401 INPUT PORT	402 INPUT LABEL	403 FEC	404 OUTPUT PORT	405 OUTPUT LABEL	406 DC FLAG
1	2	W.X.Y.Z	1	-	0
3	1	a.b.c.d	1	-	0

FIG.11

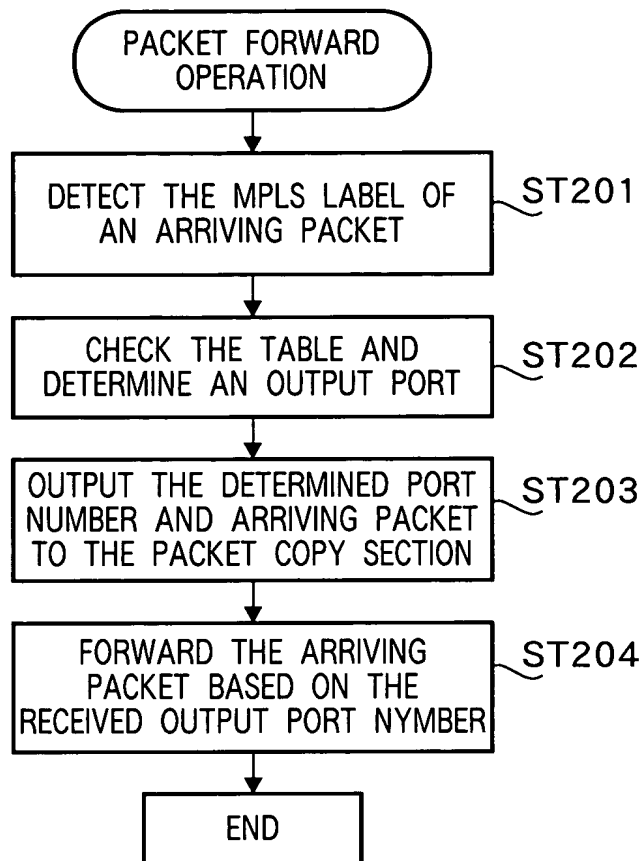


FIG.12

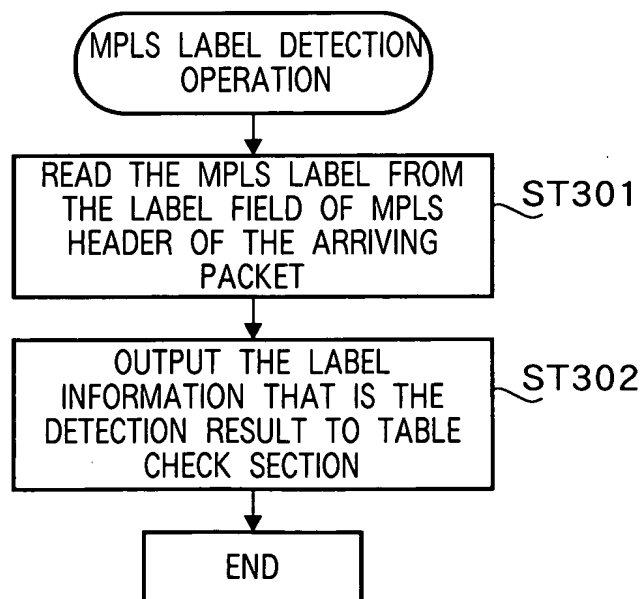


FIG.13

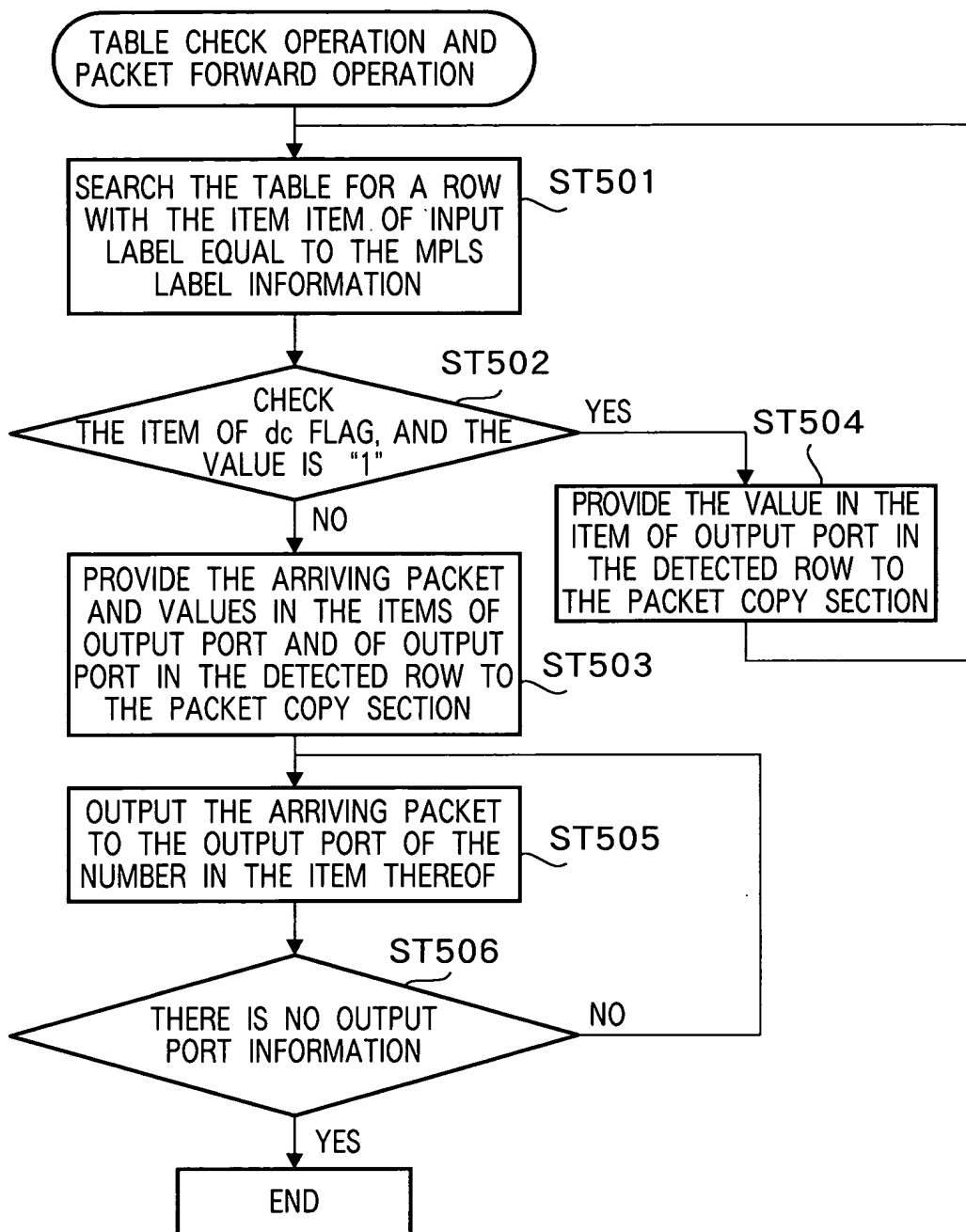


FIG.14

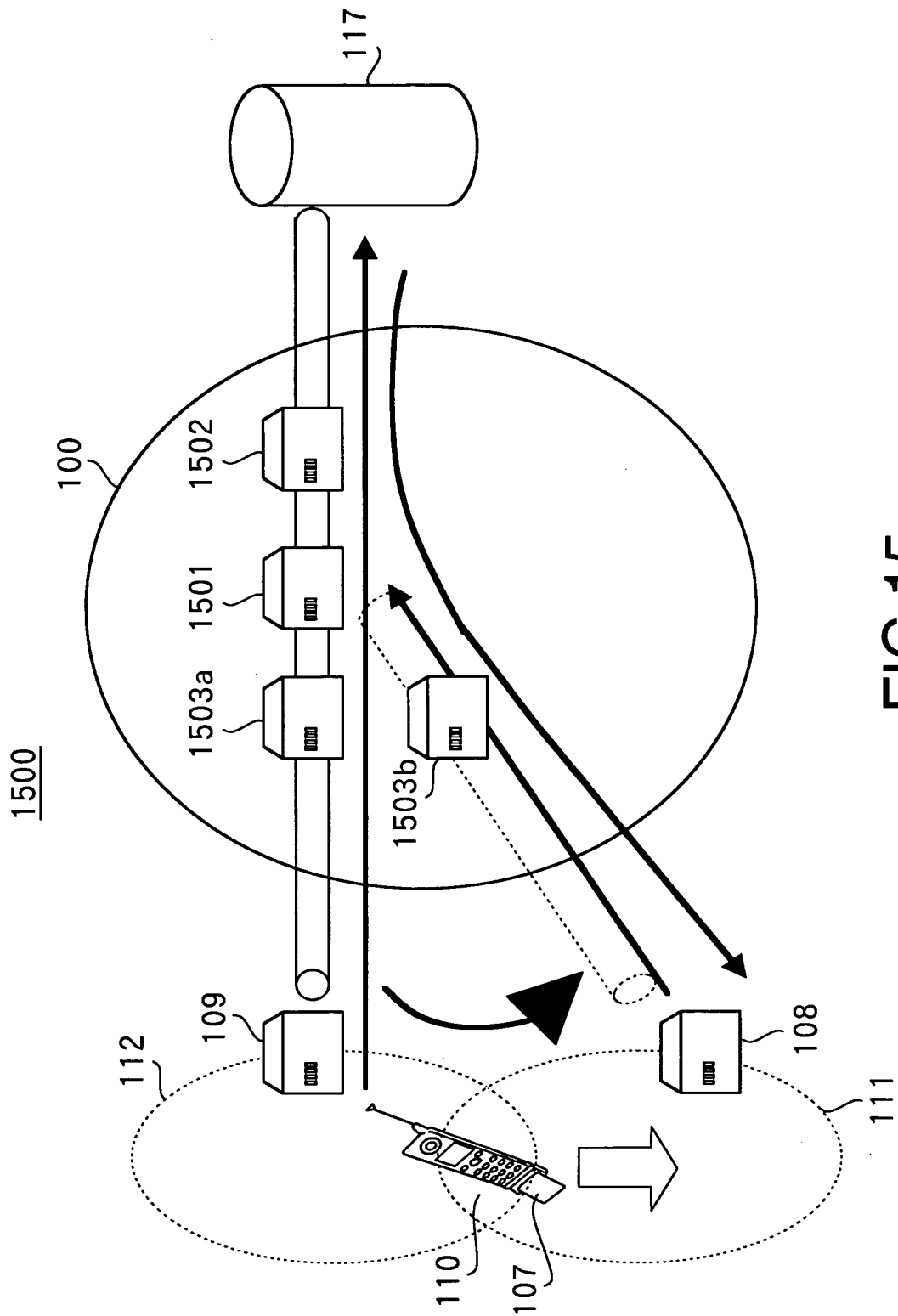


FIG.15

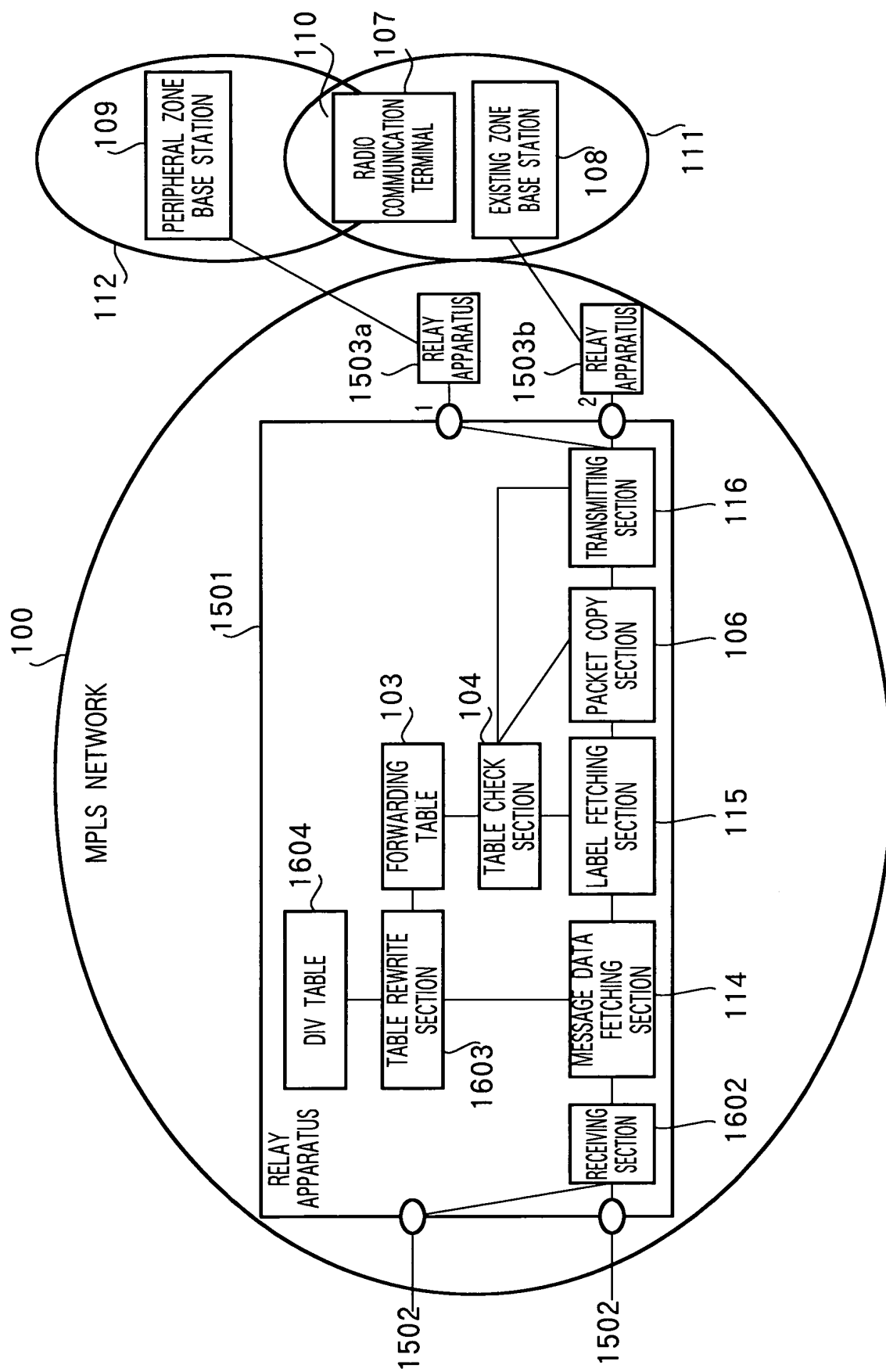


FIG.16

1604

1701	1702	1703	1704
FEC	OUTPUT PORT	OUTPUT LABEL	DC FLAG
a.b.c.d	1	2	1
W.X.Y.Z	2	3	0
a.b.c.d	2	1	0

FIG.17

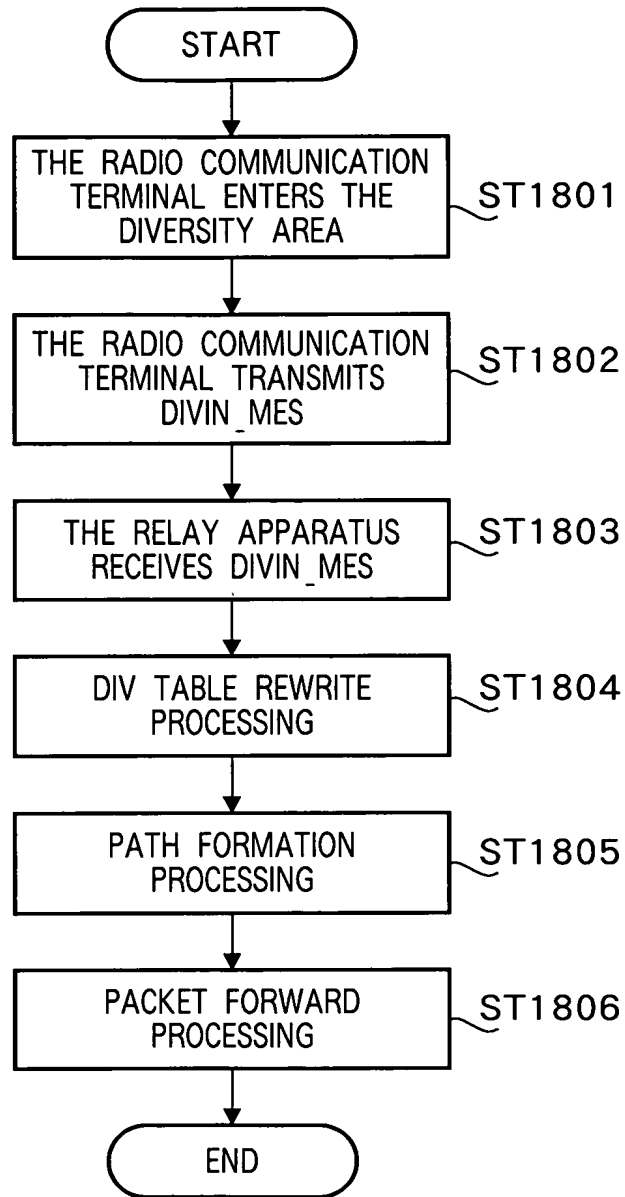


FIG.18

1900

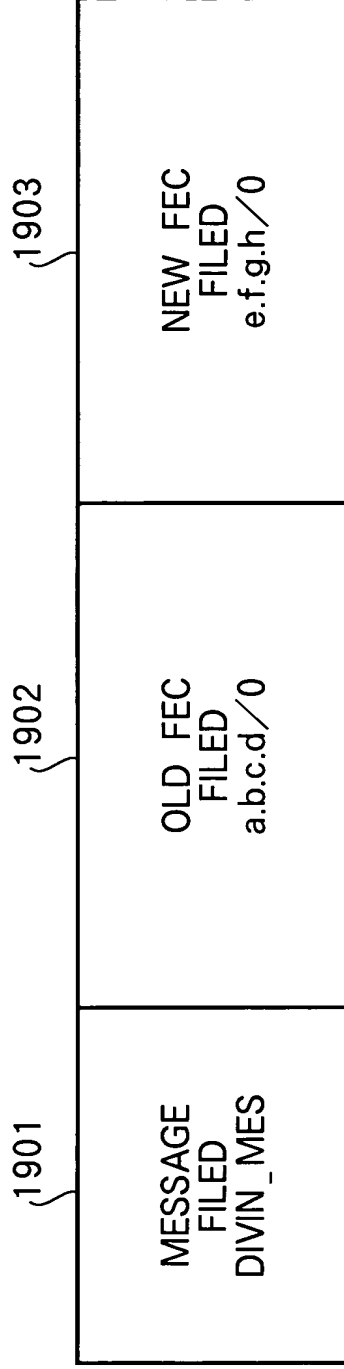


FIG.19

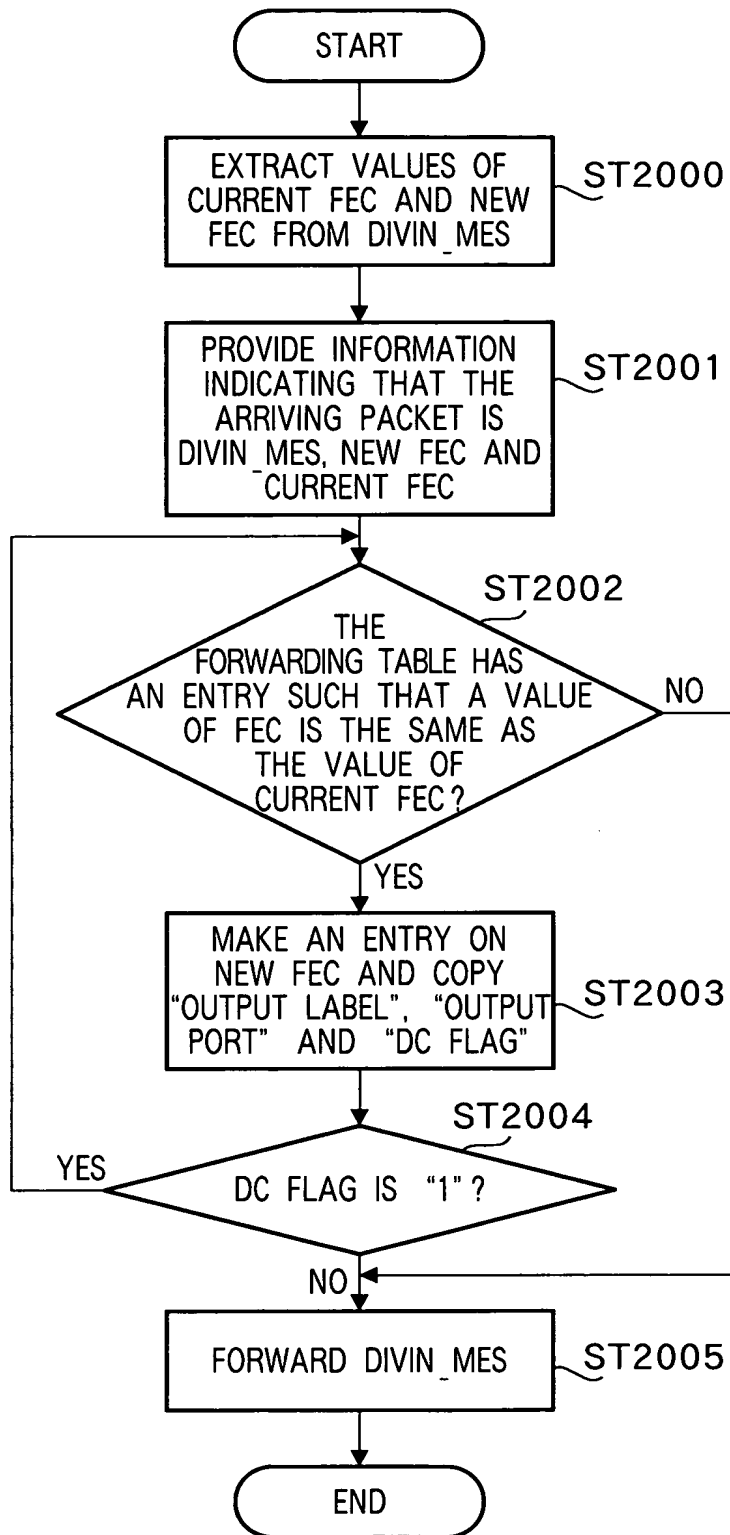


FIG.20

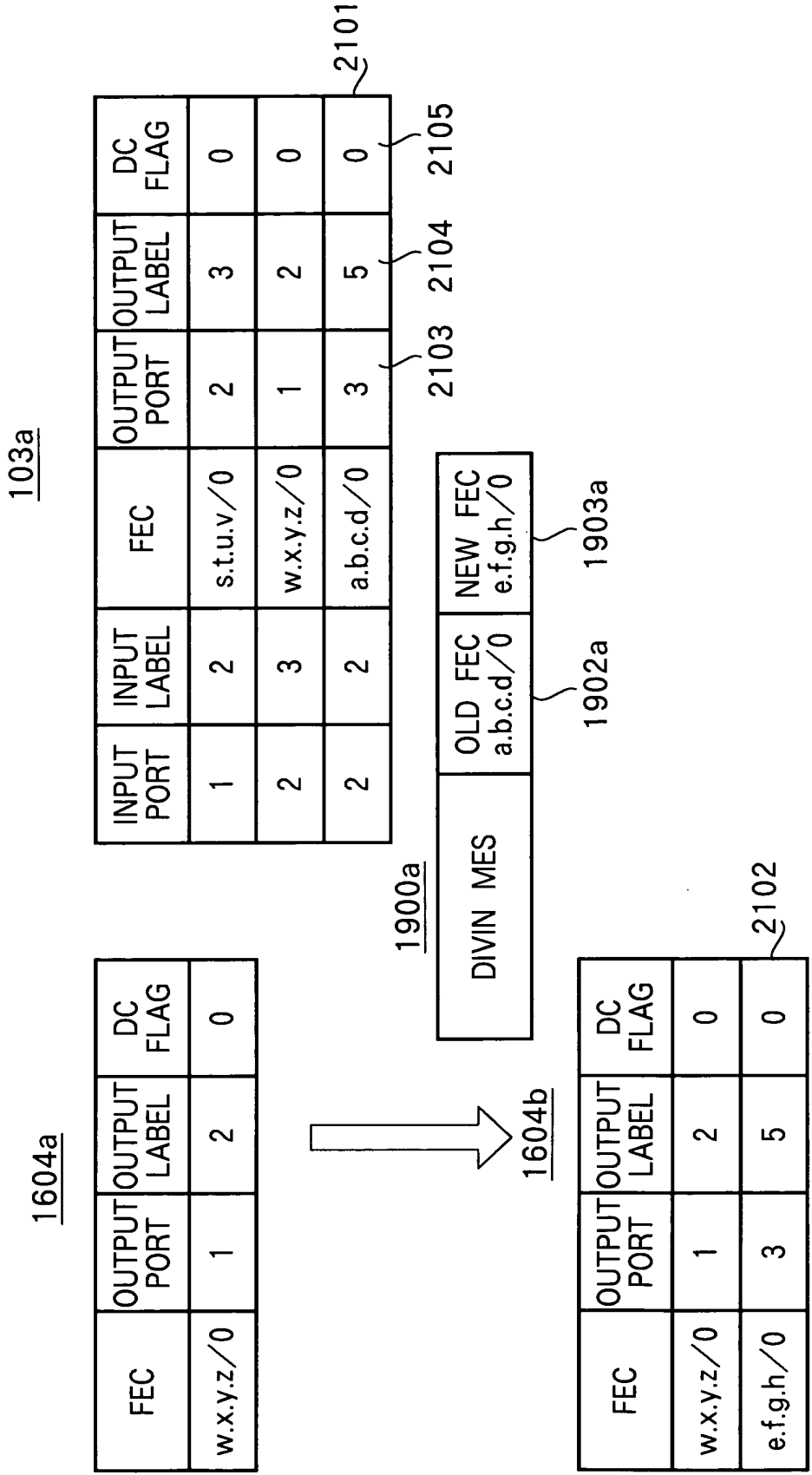


FIG.21

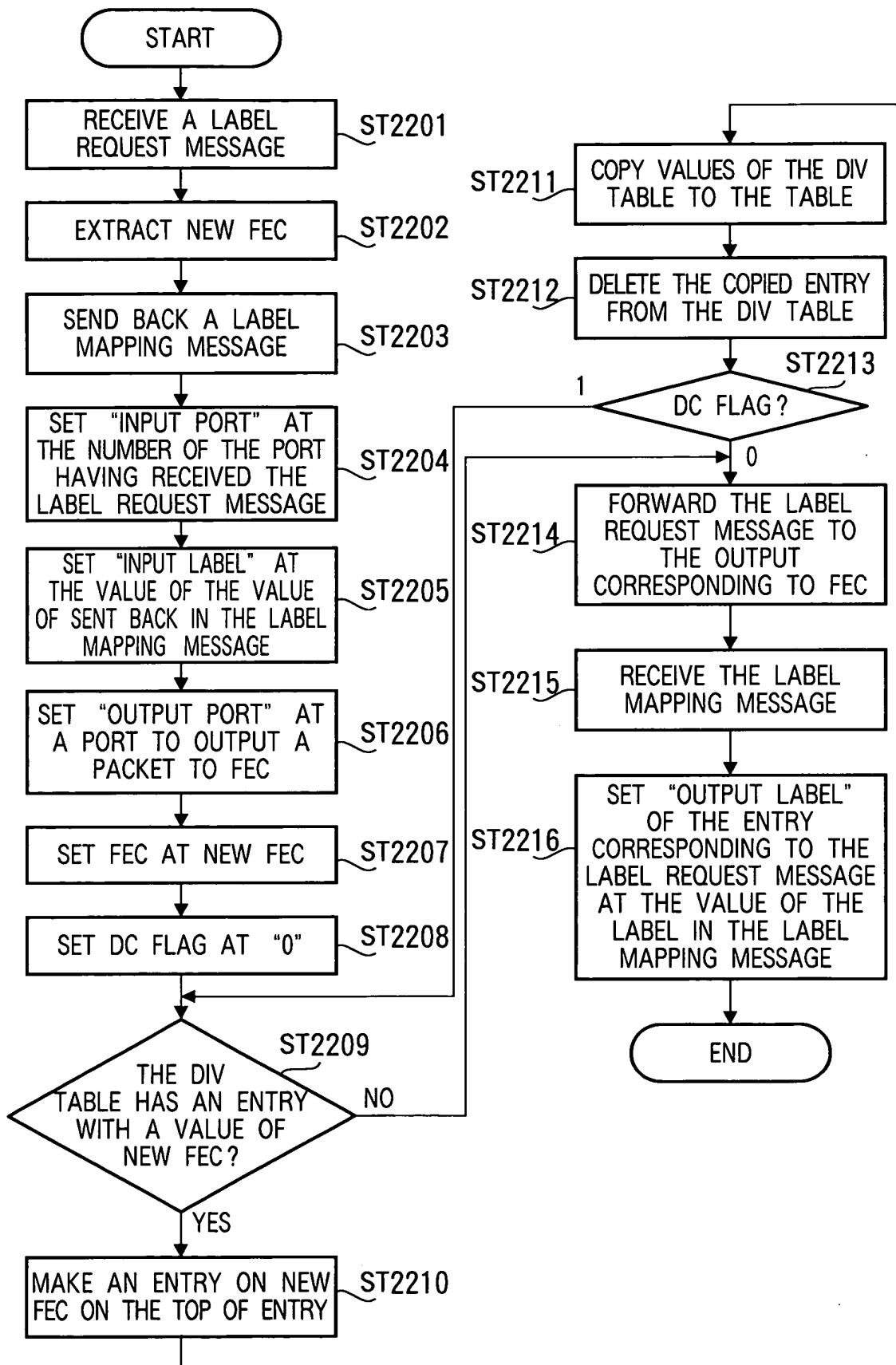


FIG.22

103b

2302		2302		2303	
401a		402a	403a	404a	405a
INPUT PORT	INPUT LABEL	FEC	OUTPUT PORT	OUTPUT LABEL	DC FLAG
2	5	e.f.g.h/0	3	5	1
2	5	e.f.g.h/0	4	-	0
1	2	s.t.u.v/0	2	3	0
2	3	w.x.y.z/0	1	2	0
2	2	a.b.c.d/0	3	5	0

1604b

FEC	OUTPUT PORT	OUTPUT LABEL	DC FLAG
w.x.y.z/0	1	2	0
e.f.g.h/0	3	5	0

FIG.23

1604c

FEC	OUTPUT PORT	OUTPUT LABEL	DC FLAG
w.x.y.z/0	1	2	0

103c

401a {		402a {	403a {	404a {	405a {	406a {
INPUT PORT	INPUT LABEL	FEC	OUTPUT PORT	OUTPUT LABEL	DC FLAG	
2	5	e.f.g.h/0	3	5	1	
2	5	e.f.g.h/0	4	8	0	
1	2	s.t.u.v/0	2	3	0	
2	3	w.x.y.z/0	1	2	0	
2	2	a.b.c.d/0	3	5	0	

2302

2301

2401

FIG.24

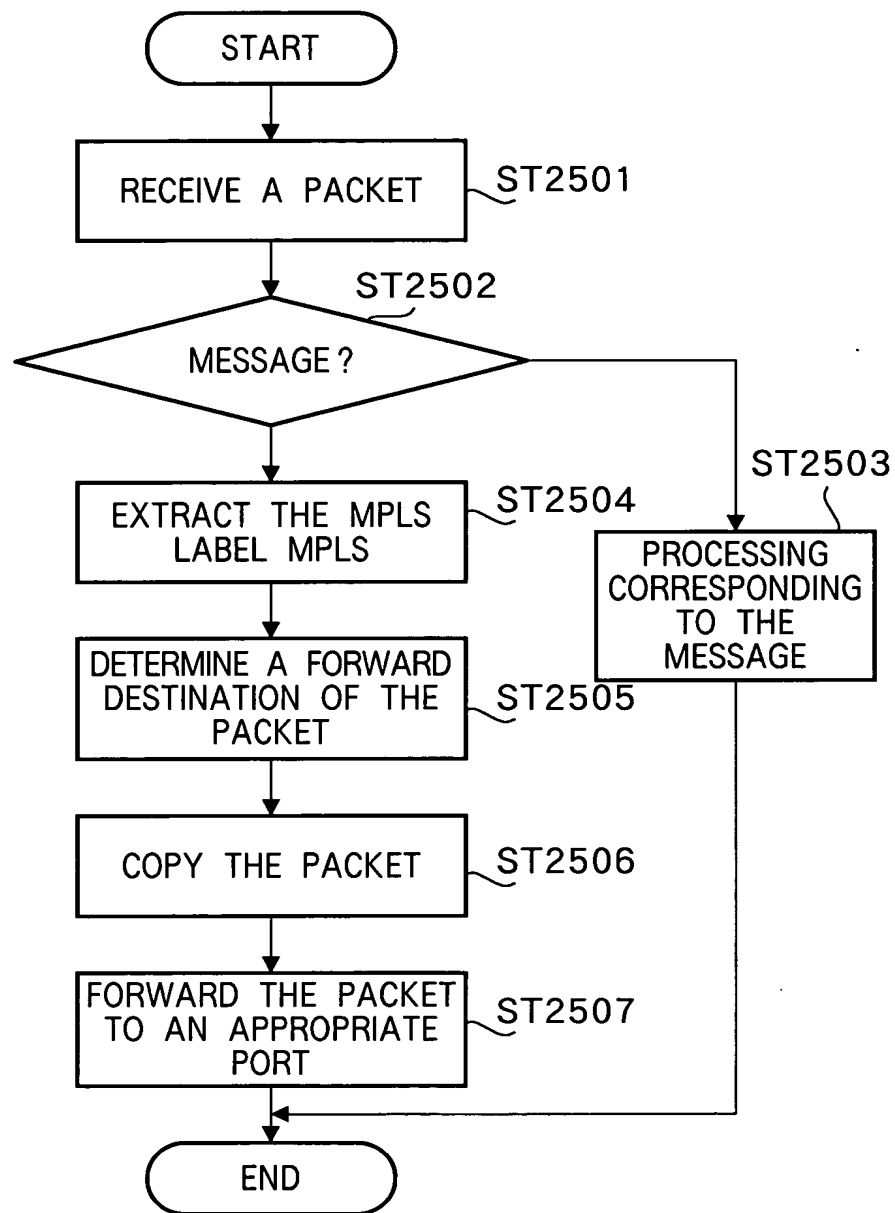


FIG.25

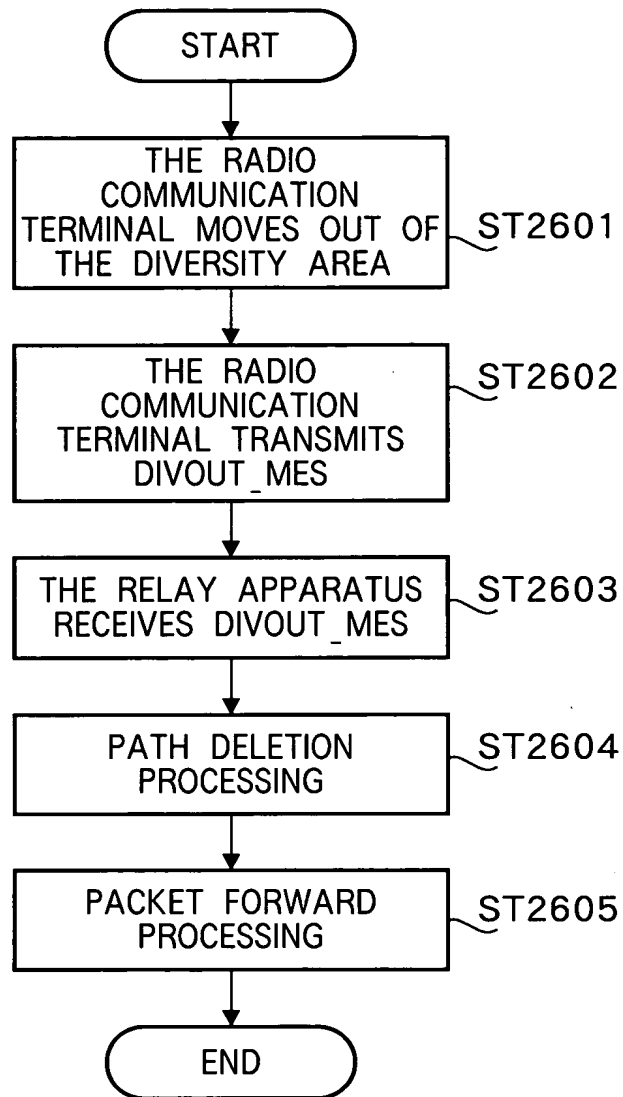


FIG.26

2700

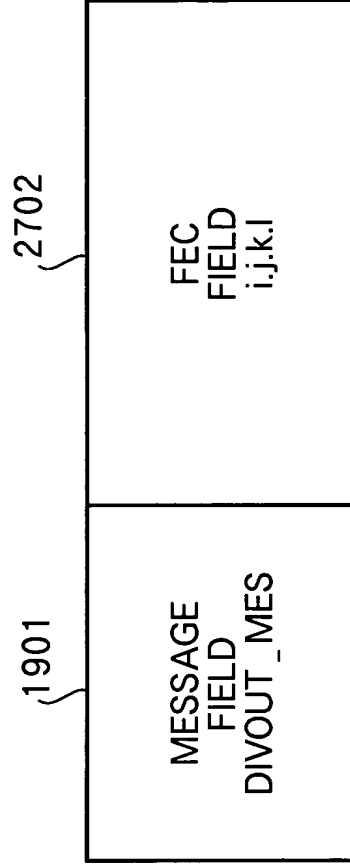


FIG.27

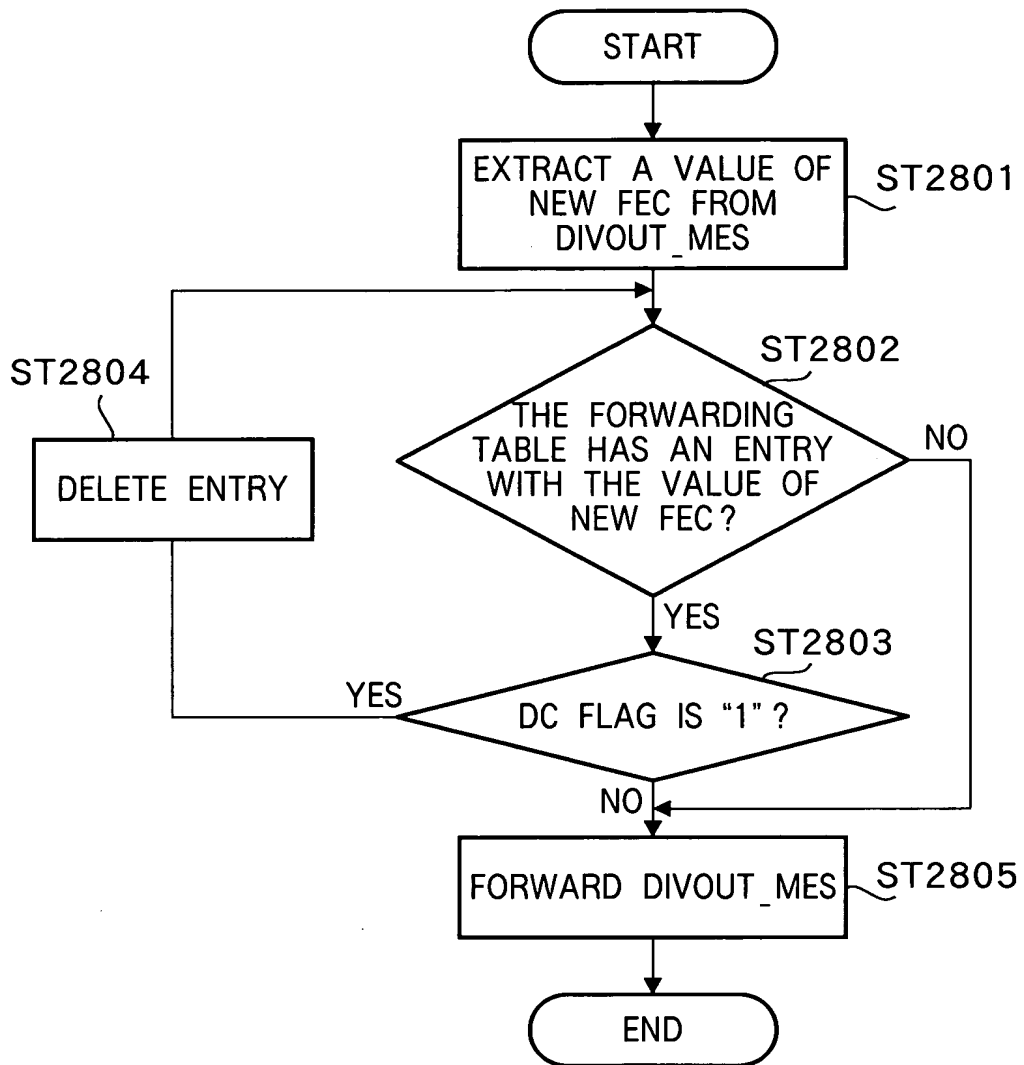


FIG.28

2900

2901

DIVOUT_MES	FEC e.f.g.h/0
------------	------------------

103d

DIV TABLE

FEC	OUTPUT PORT	OUTPUT LABEL	DC FLAG
w.x.y.z/0	1	2	0

TABLE

INPUT PORT	INPUT LABEL	FEC	OUTPUT PORT	OUTPUT LABEL	DC FLAG
2	5	e.f.g.h/0	4	8	0
1	2	s.t.u.v/0	2	3	0
2	3	w.x.y.z/0	1	2	0
2	2	a.b.c.d/0	3	5	0

406a

FIG.29

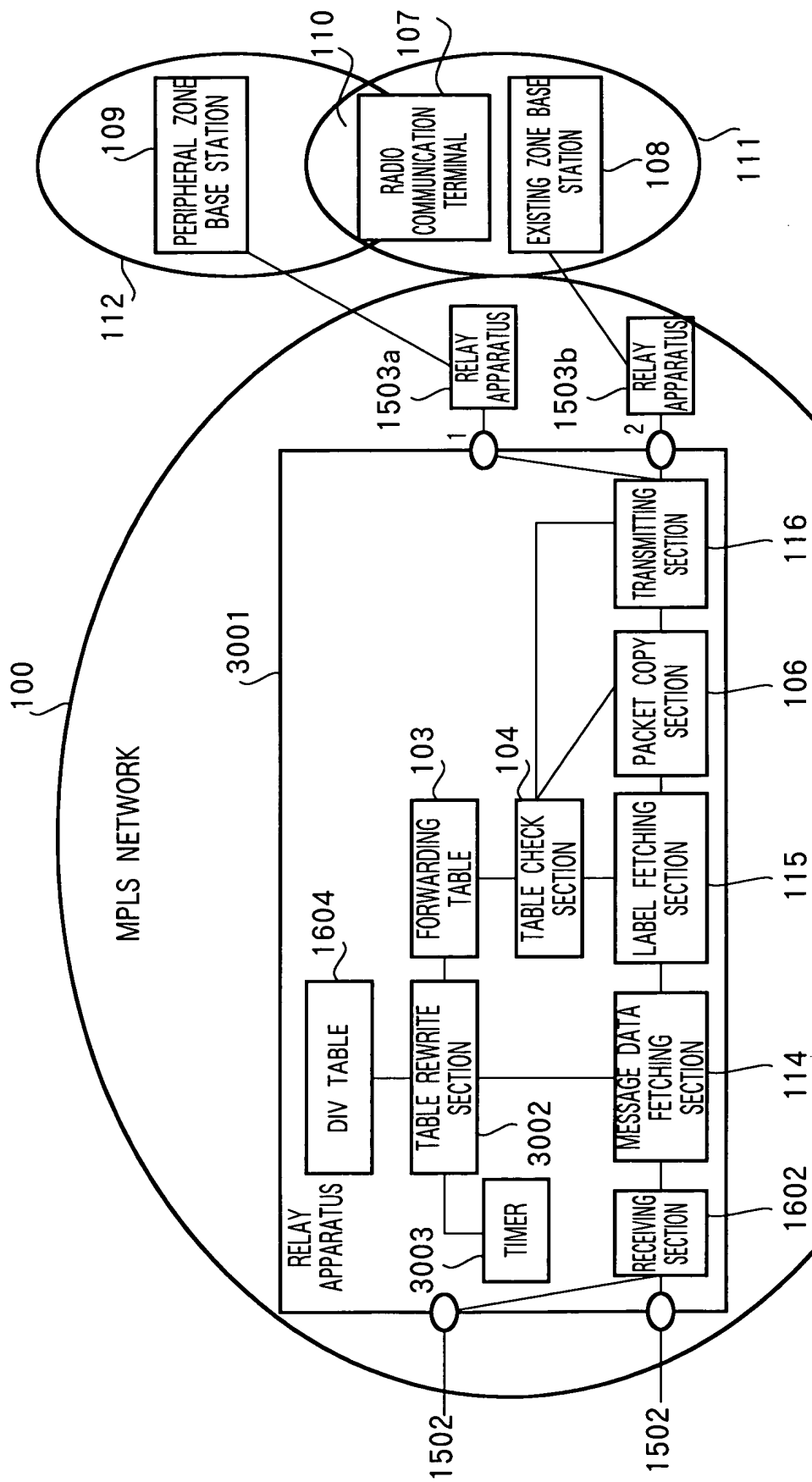


FIG.30